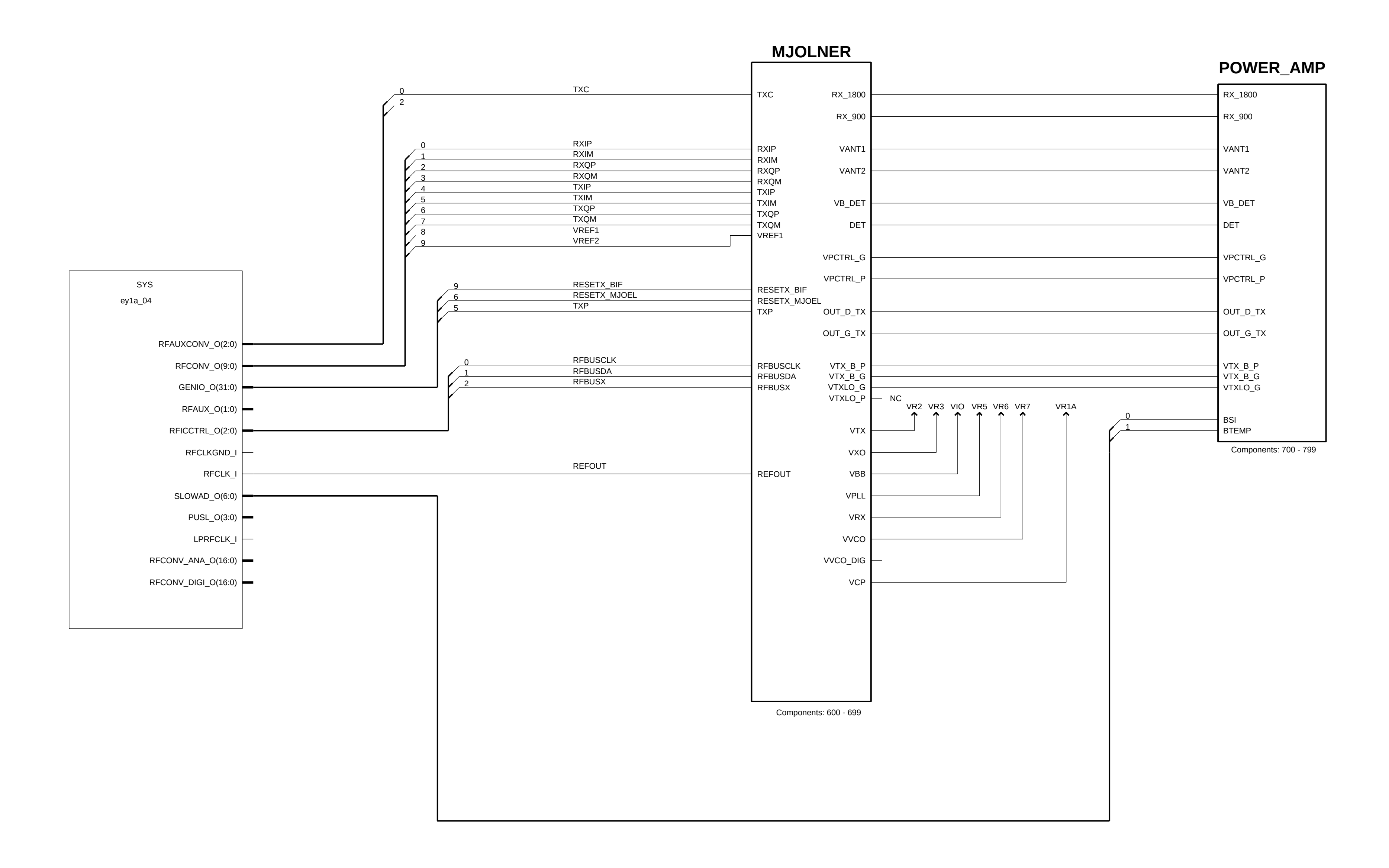
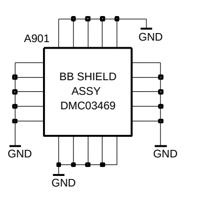
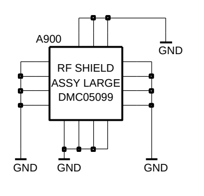
Module top sheet

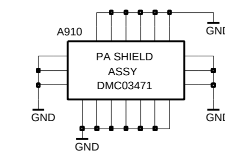


Shielding's cans

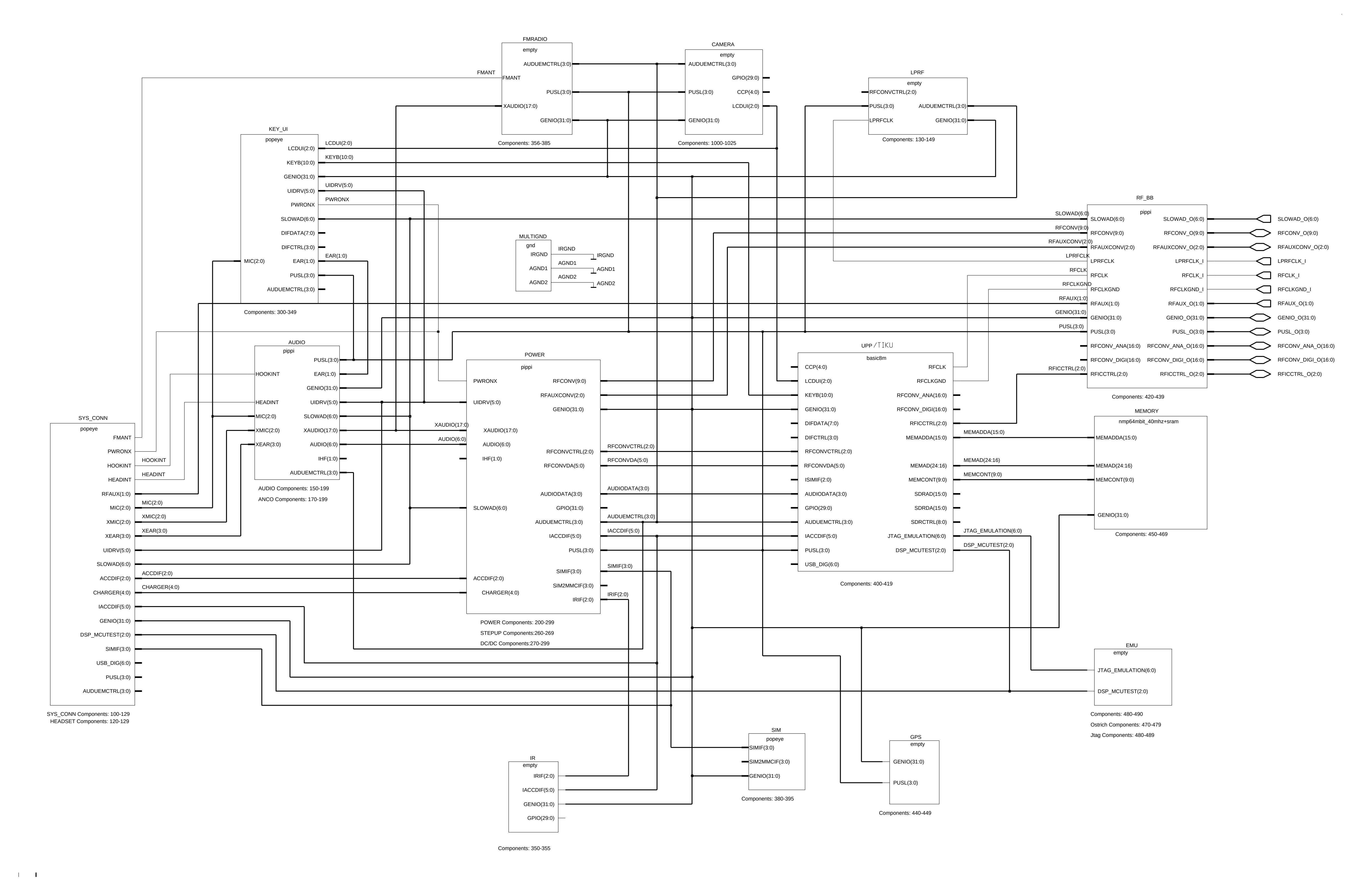






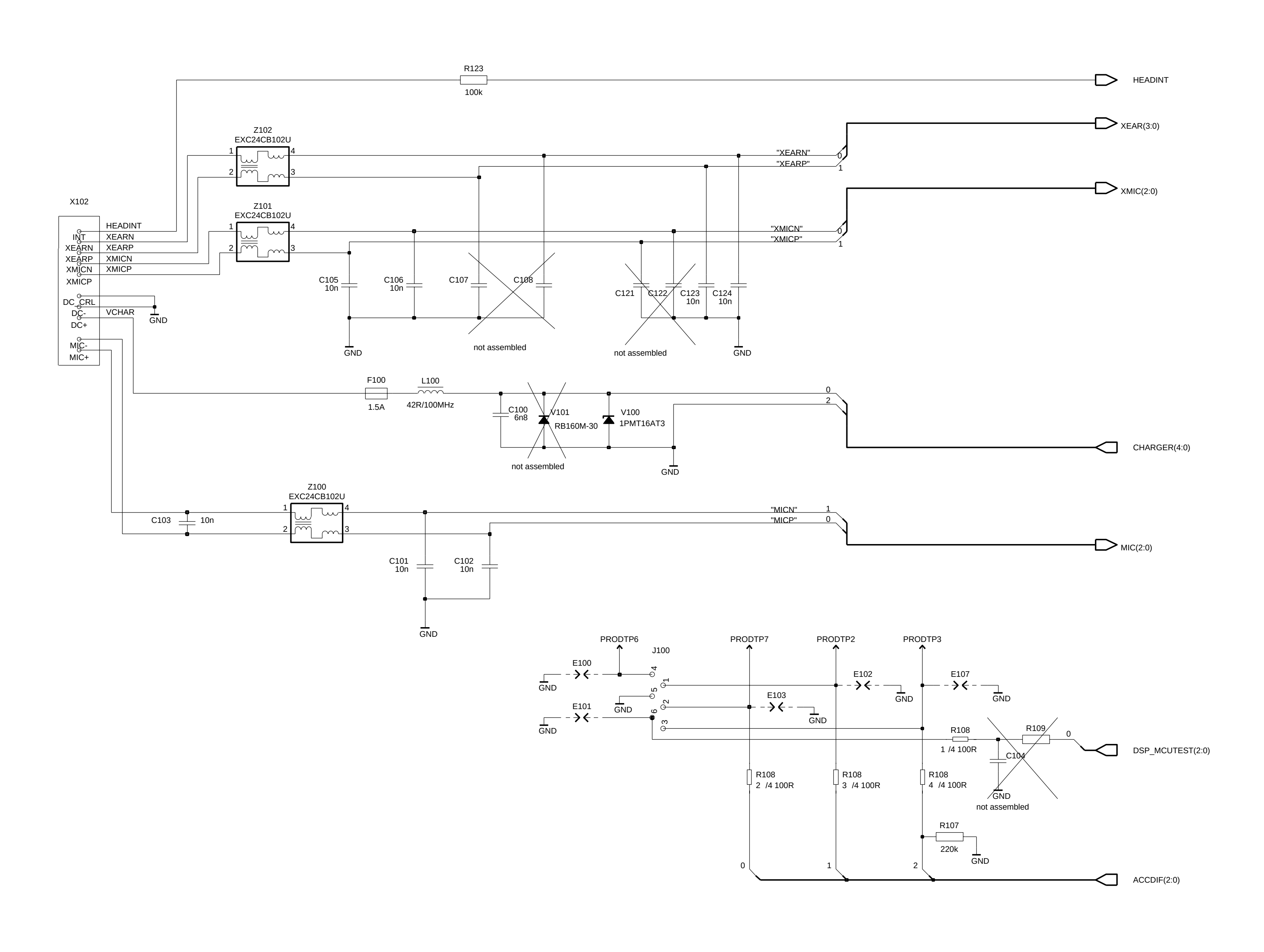


BB top sheet

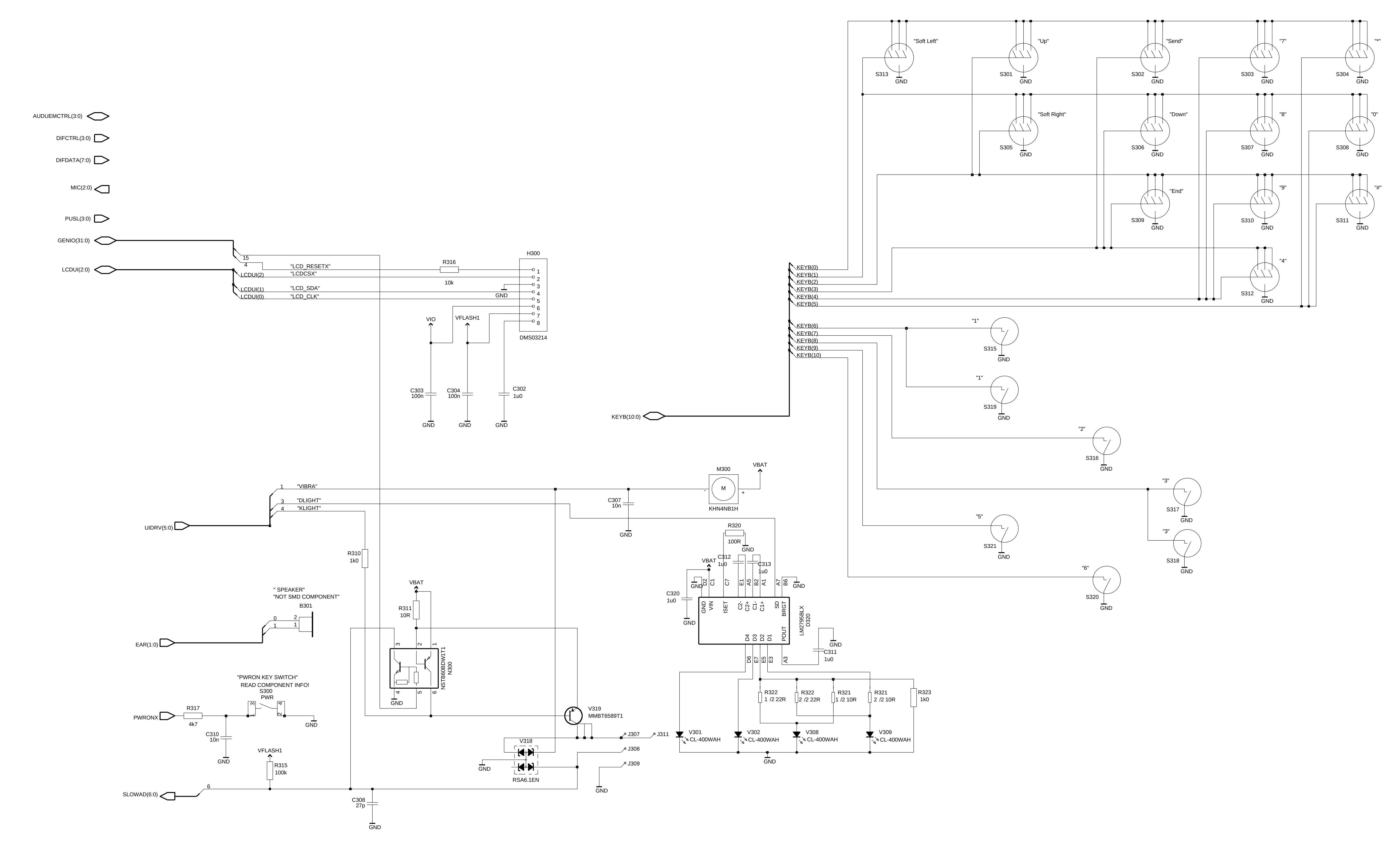

FMRADIO
CAMERA
LPRF
KEY_UI
MULTIGND
RF_BB
AUDIO
POWER
UPP /TIKU
MEMORY
SYS_CONN
EMU
SIM
GPS
IR

System connector

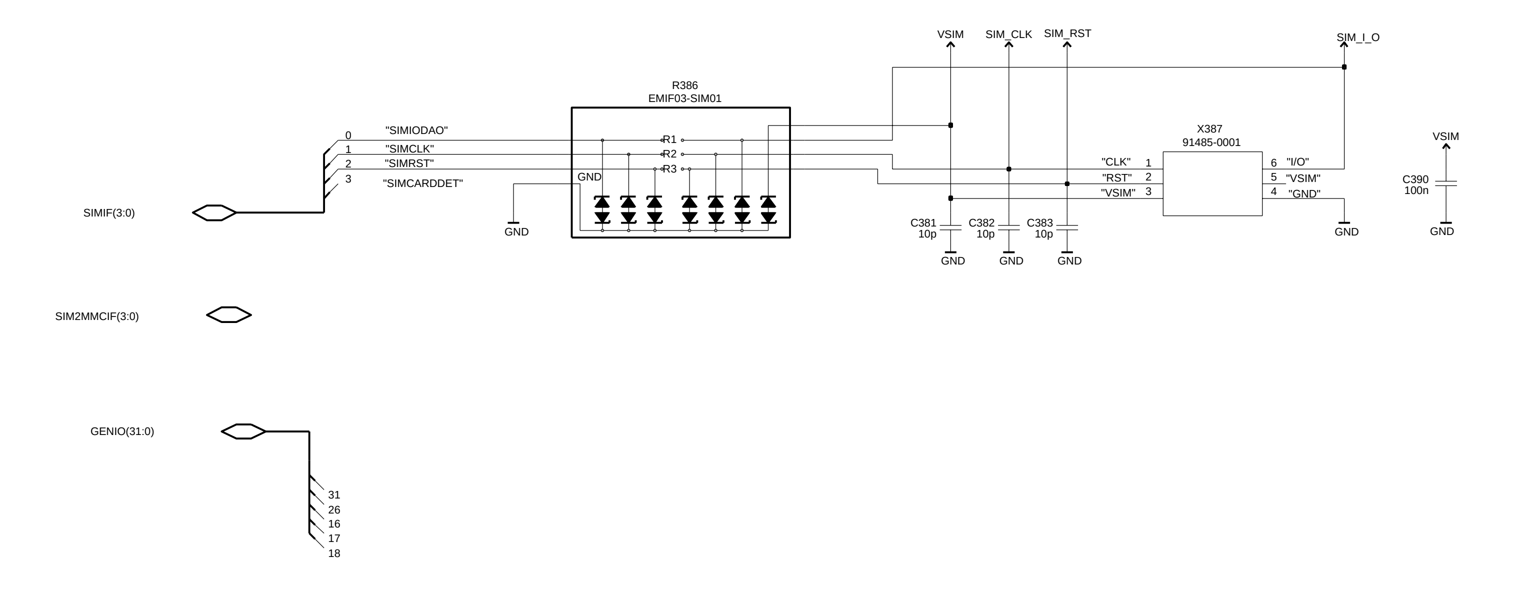
UIDRV(5:0)
IACCDIF(5:0)
RFAUX(1:0)
PWRONX
SIMIF(3:0)
HOOKINT
GENIO(31:0)
FMANT
SLOWAD(6:0)
USB_DIG(6:0)
PUSL(3:0)
AUDUEMCTRL(3:0)



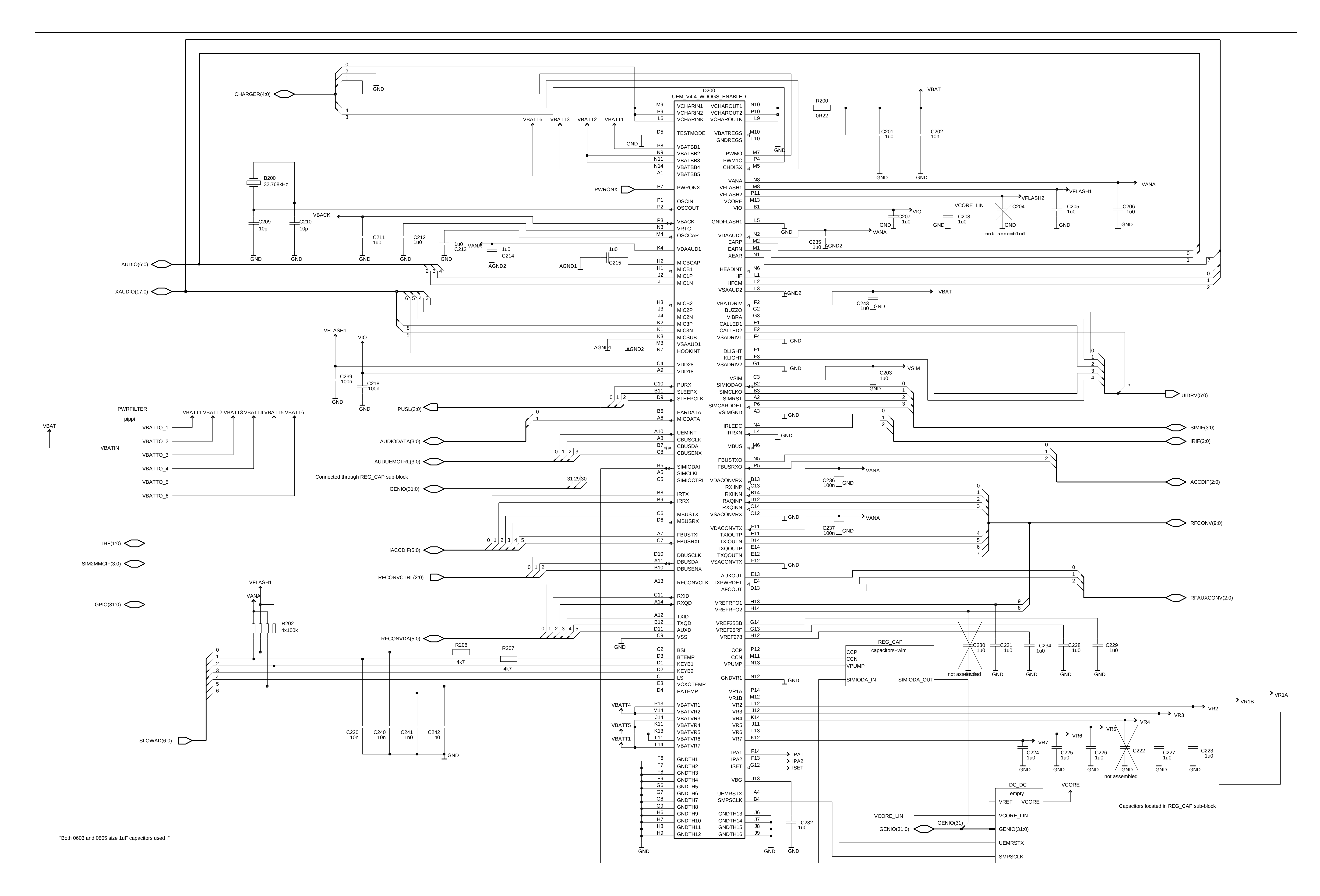
U/I, key and functional cover



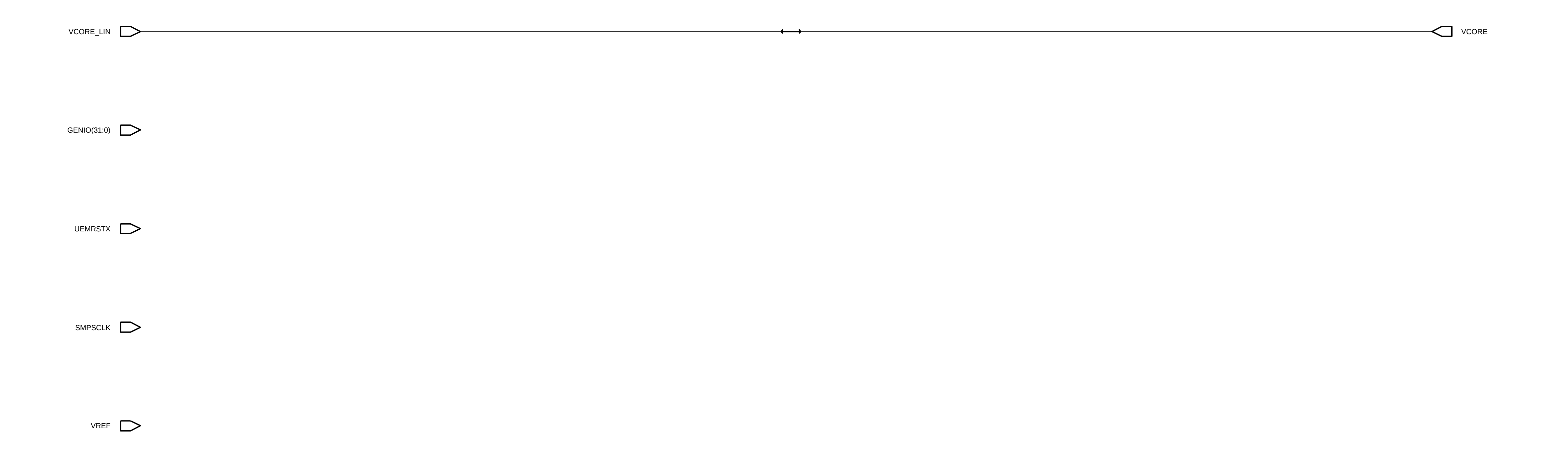
SIM


SIMIF(3:0)
0
1
2
3
"SIMIODAO"
"SIMCLK"
"SIMRST"
"SIMCARDDET"
GND
R386
EMIF03-SIM01
R1
R2
R3
GND
VSIM
SIM_CLK
SIM_RST
SIM_I_O
C381
10p
C382
10p
C383
10p
GND
GND
GND
X387
91485-0001
"CLK" 1
"RST" 2
"VSIM" 3
6 "I/O"
5 "VSIM"
4 "GND"
GND
VSIM
C390
100n
GND
SIM2MMCIF(3:0)
GENIO(31:0)
31
26
16
17
18

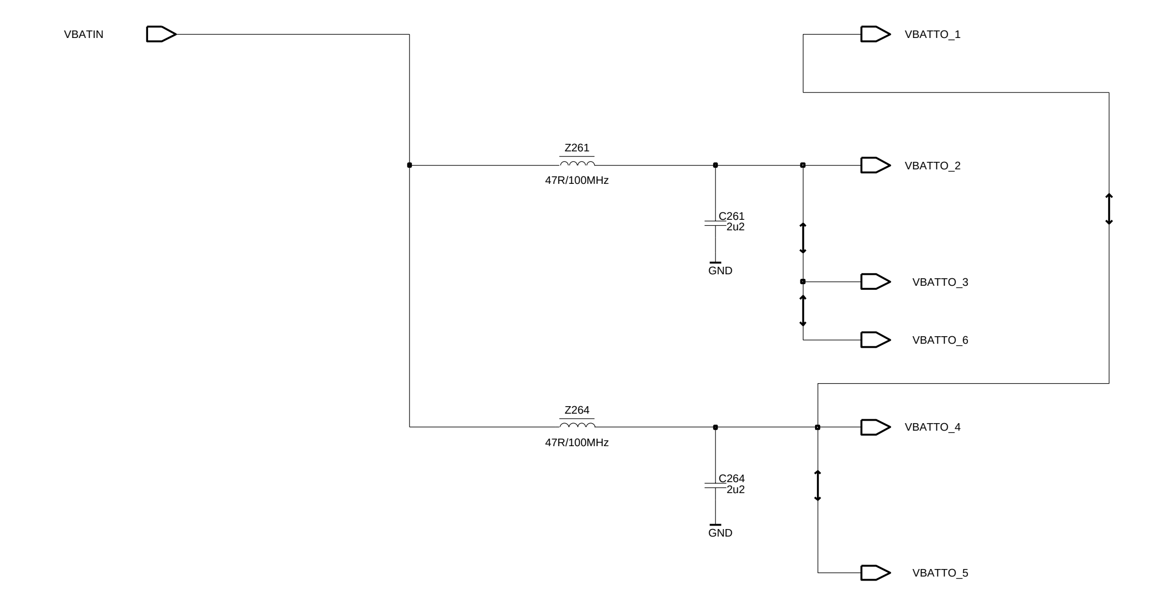
UEM


D200
UEM_V4.4_WDOGS_ENABLED
CHARGER(4:0)
AUDIO(6:0)
XAUDIO(17:0)
PWRFILTER
pippi
VBATIN
VBAT
PUSL(3:0)
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
Connected through REG_CAP sub-block
GENIO(31:0)
IHF(1:0)
SIM2MMCIF(3:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
GPIO(31:0)
RFCONVDA(5:0)
SLOWAD(6:0)
UIDRV(5:0)
SIMIF(3:0)
IRIF(2:0)
ACCDIF(2:0)
RFCONV(9:0)
RFAUXCONV(2:0)
REG_CAP
capacitors+wim
DC_DC
empty
not assembled
Capacitors located in REG_CAP sub-block
"Both 0603 and 0805 size 1uF capacitors used !"

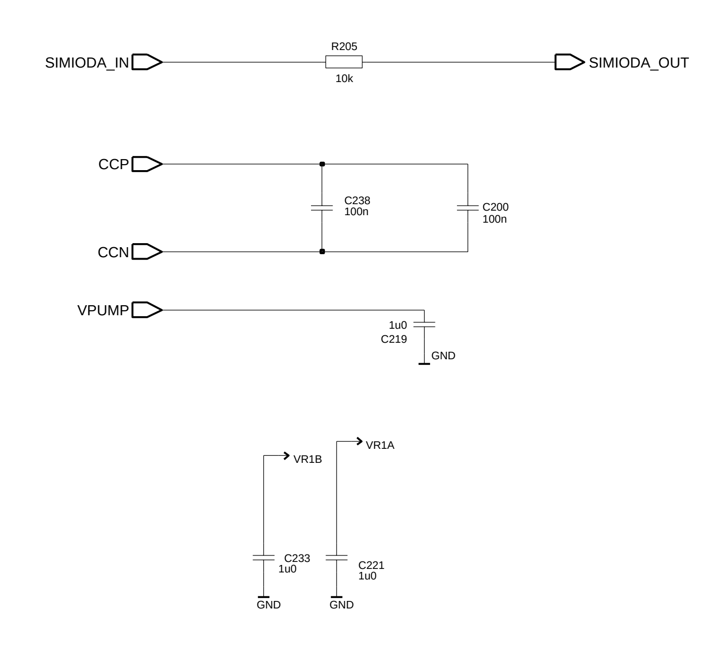
Vcore



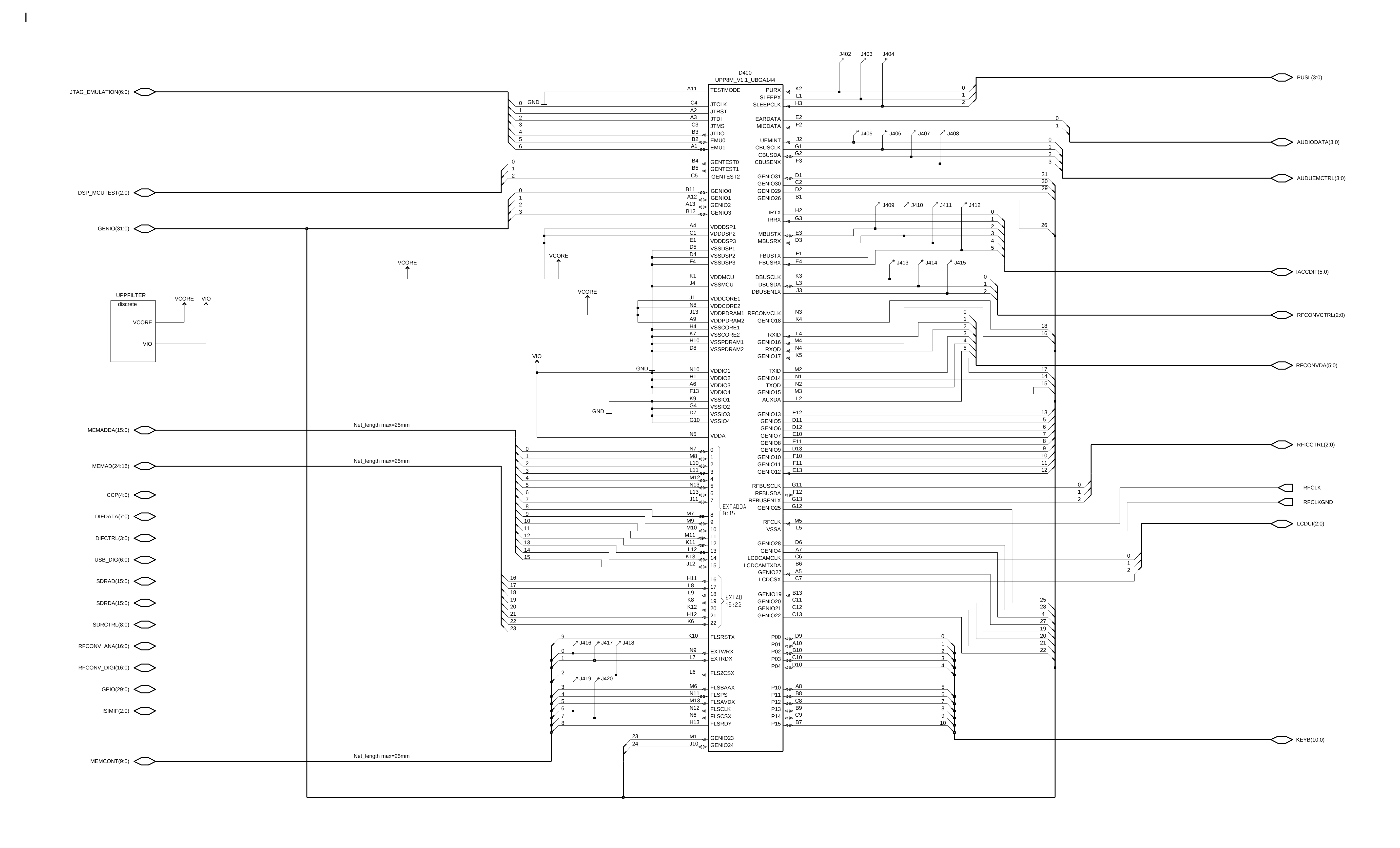
Vbat filter


VBATIN
VBATTO_1
Z261
47R/100MHz
VBATTO_2
C261
2u2
GND
VBATTO_3
VBATTO_6
Z264
47R/100MHz
VBATTO_4
C264
2u2
GND
VBATTO_5

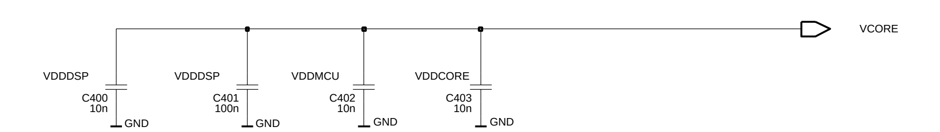
UEM charge pump

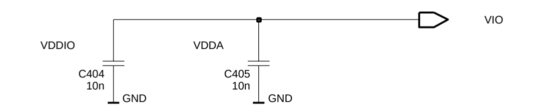


UPP

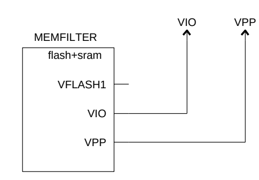


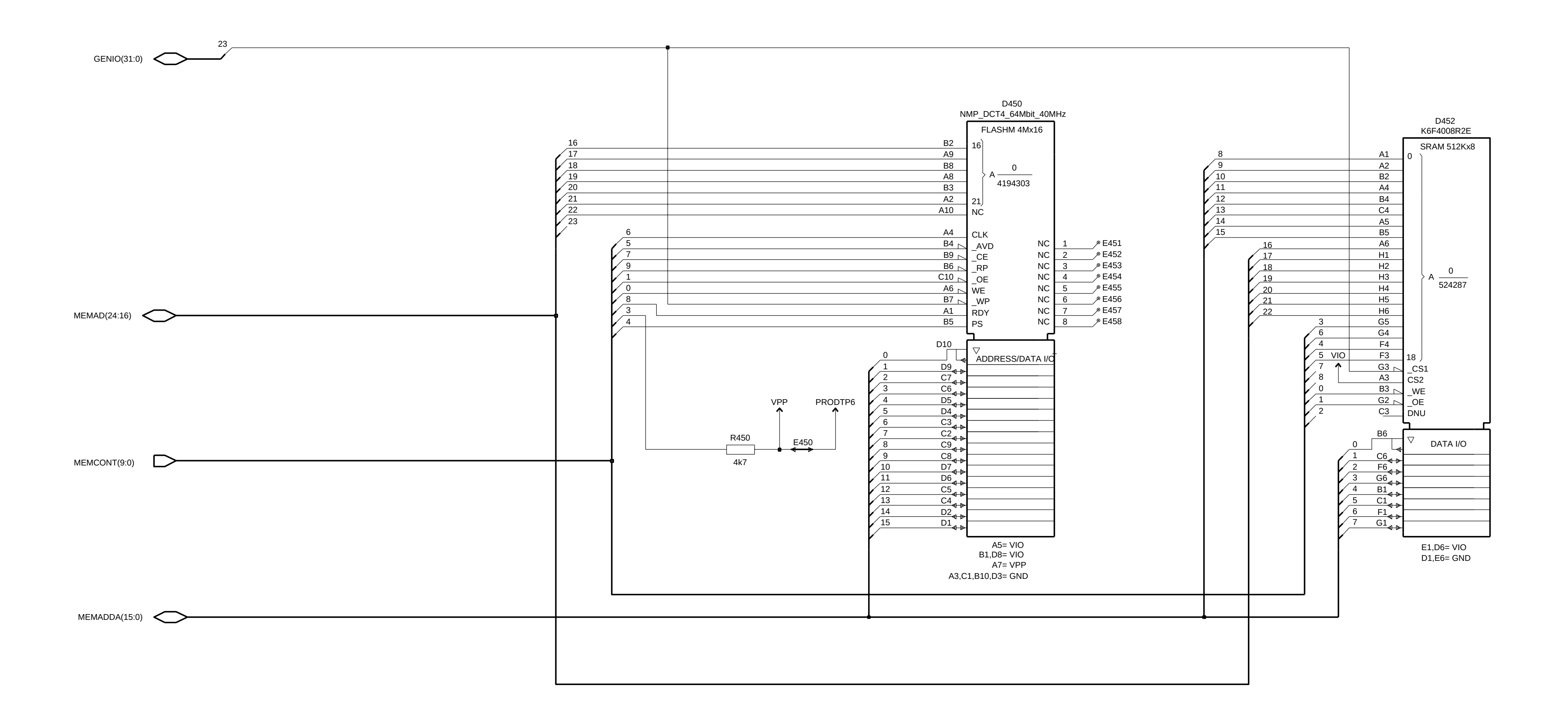
UPP decoupling





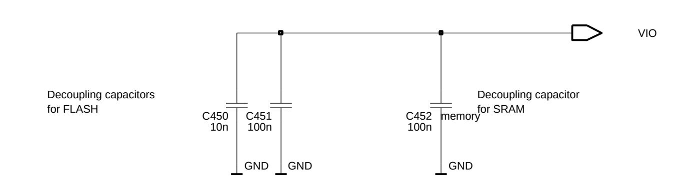
Memory


MEMFILTER
flash+sram
VFLASH1
VIO
VPP
VIO
VPP


GENIO(31:0)
MEMAD(24:16)
MEMCONT(9:0)
MEMADDA(15:0)
D450
NMP_DCT4_64Mbit_40MHz
FLASHM 4Mx16
4194303
CLK
_AVD
_CE
_RP
_OE
WE
_WP
RDY
PS
ADDRESS/DATA I/O
A5= VIO
B1,D8= VIO
A7= VPP
A3,C1,B10,D3= GND
D452
K6F4008R2E
SRAM 512Kx8
524287
_CS1
CS2
_WE
_OE
DNU
DATA I/O
E1,D6= VIO
D1,E6= GND
R450
4k7
E450
VPP
PRODTP6
E451
E452
E453
E454
E455
E456
E457
E458

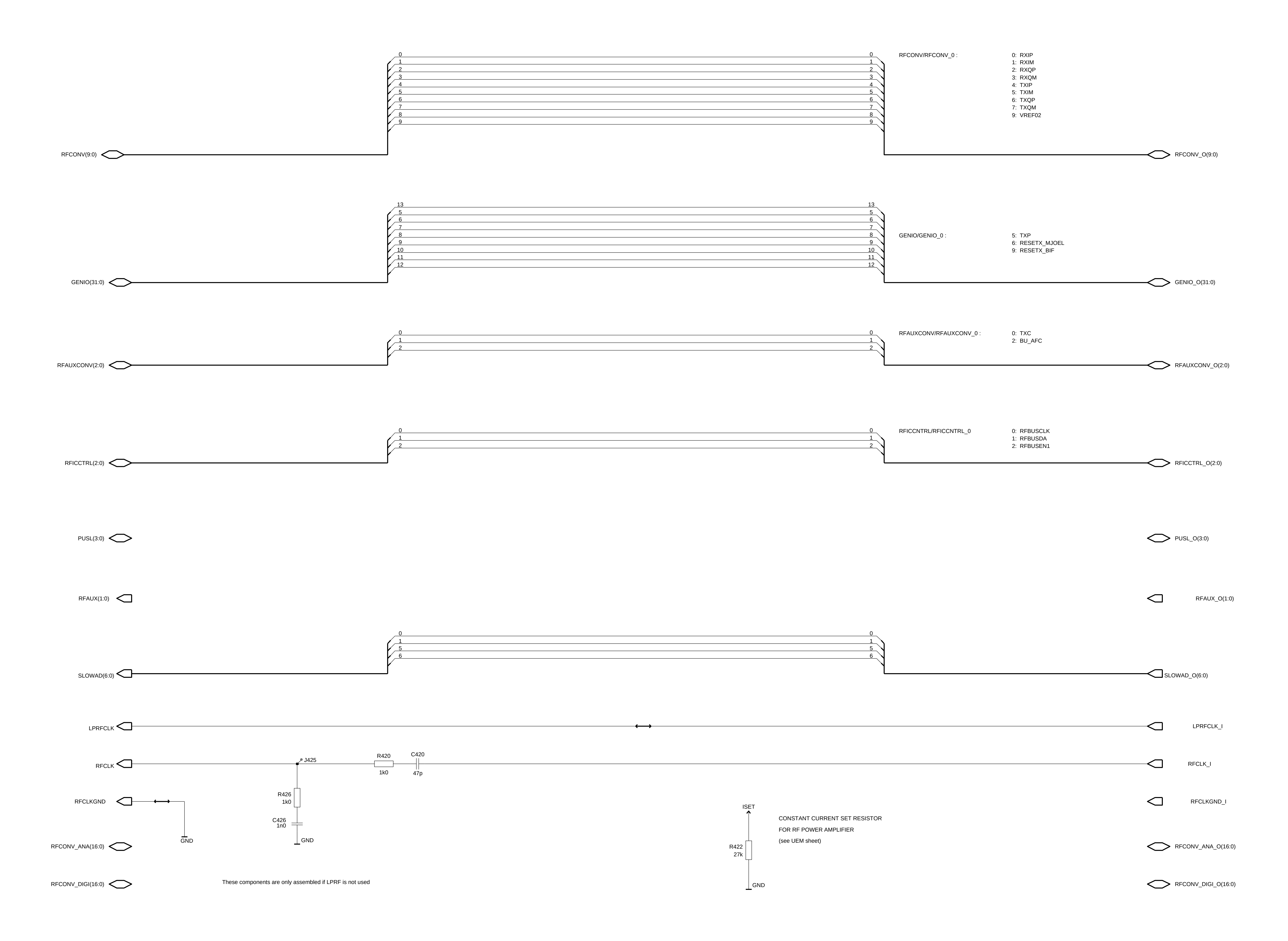
Memory decoupling

VFLASH1



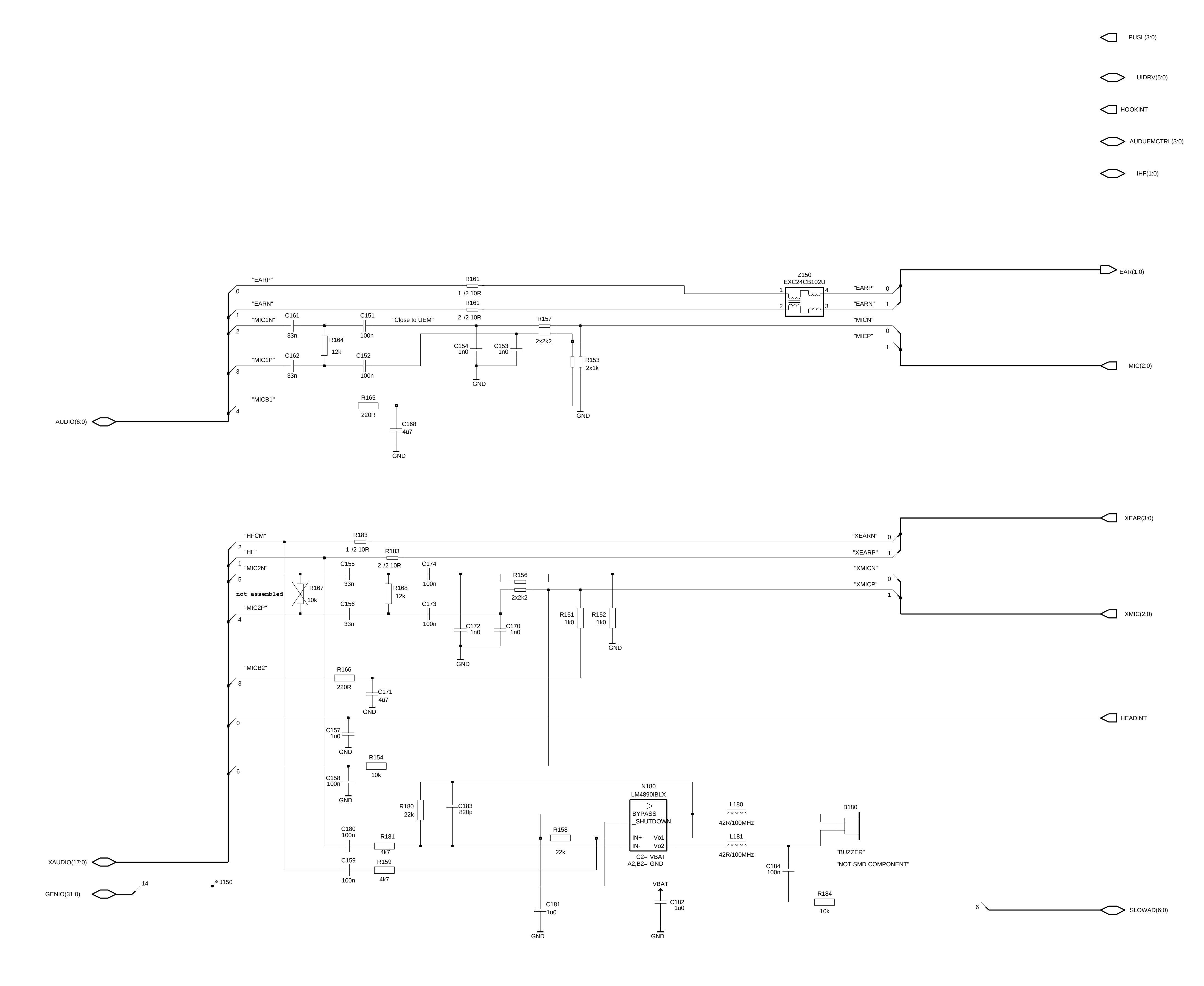


BB-RF



RFCONV/RFCONV_0 :
0: RXIP
1: RXIM
2: RXQP
3: RXQM
4: TXIP
5: TXIM
6: TXQP
7: TXQM
9: VREF02
RFCONV(9:0)
RFCONV_O(9:0)
GENIO/GENIO_0 :
5: TXP
6: RESETX_MJOEL
9: RESETX_BIF
GENIO(31:0)
GENIO_O(31:0)
RFAUXCONV/RFAUXCONV_0 :
0: TXC
2: BU_AFC
RFAUXCONV(2:0)
RFAUXCONV_O(2:0)
RFICCNTRL/RFICCNTRL_0
0: RFBUSCLK
1: RFBUSDA
2: RFBUSEN1
RFICCTRL(2:0)
RFICCTRL_O(2:0)
PUSL(3:0)
PUSL_O(3:0)
RFAUX(1:0)
RFAUX_O(1:0)
SLOWAD(6:0)
SLOWAD_O(6:0)
LPRFCLK
LPRFCLK_I
RFCLK
J425
R420
1k0
C420
47p
RFCLK_I
RFCLKGND
RFCLKGND_I
R426
1k0
C426
1n0
GND
ISET
CONSTANT CURRENT SET RESISTOR
FOR RF POWER AMPLIFIER
(see UEM sheet)
R422
27k
RFCONV_ANA(16:0)
RFCONV_ANA_O(16:0)
RFCONV_DIGI(16:0)
RFCONV_DIGI_O(16:0)
These components are only assembled if LPRF is not used

Audio



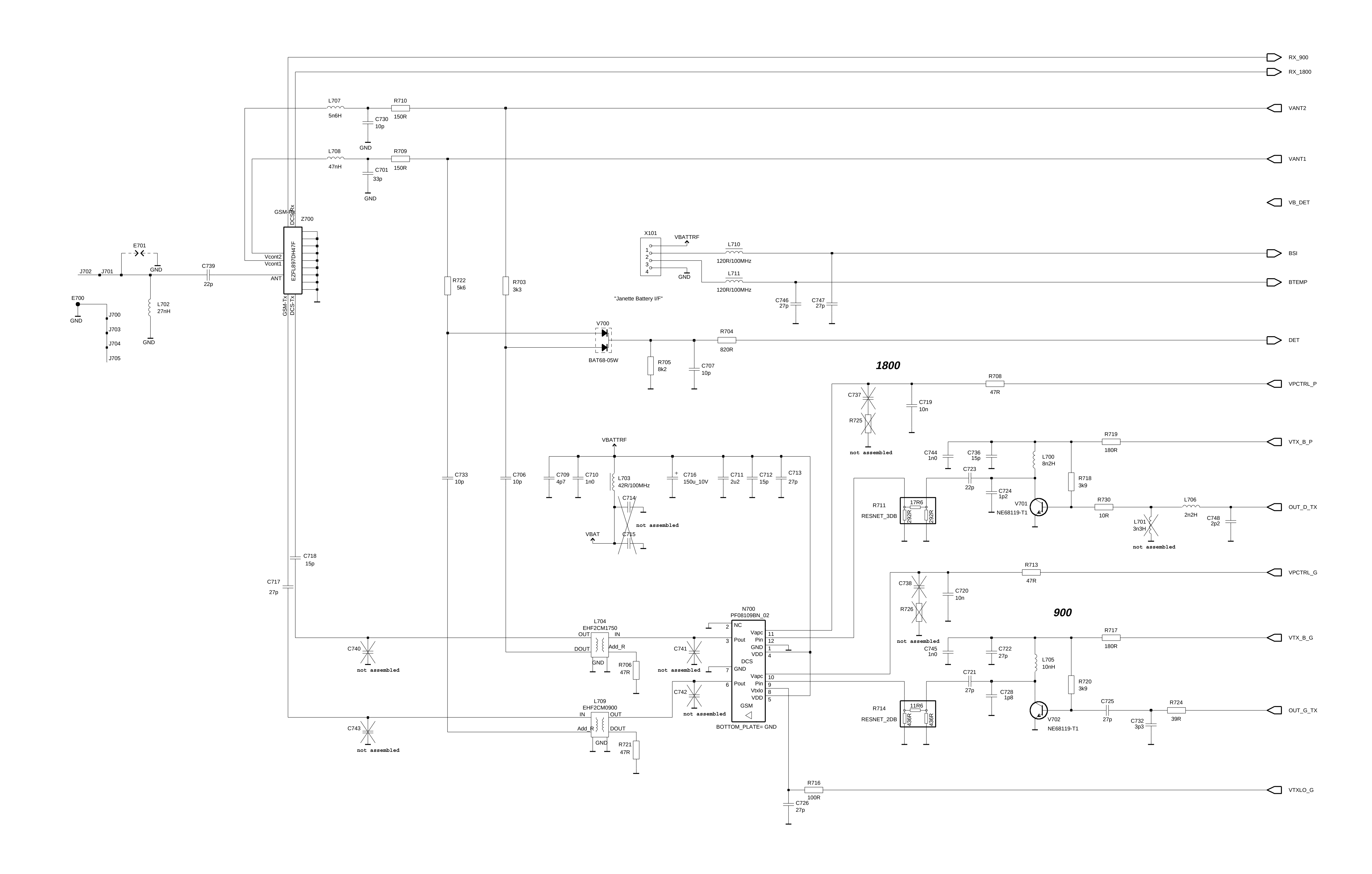
Multignd

GND IRGND

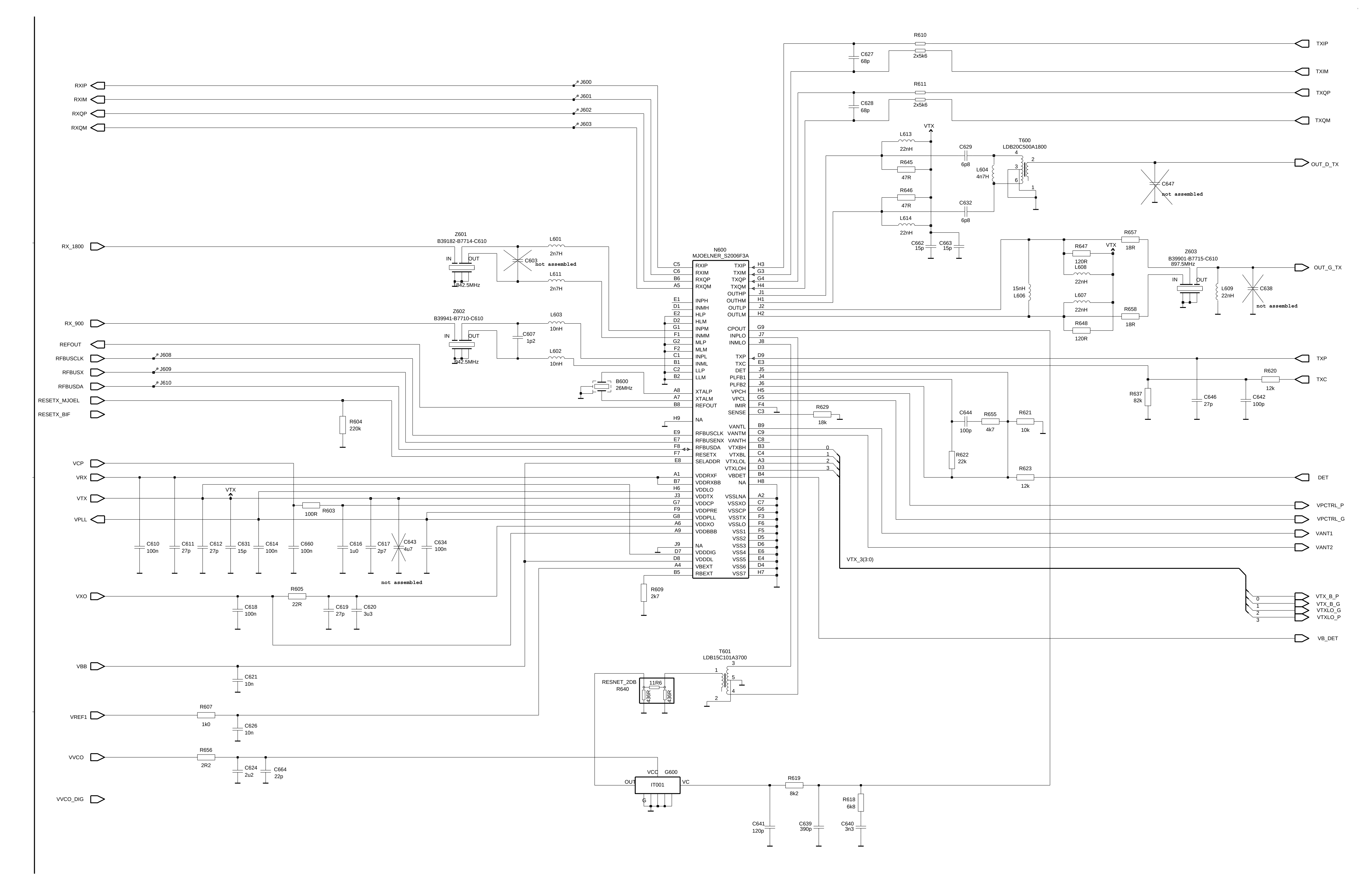
GND AGND1

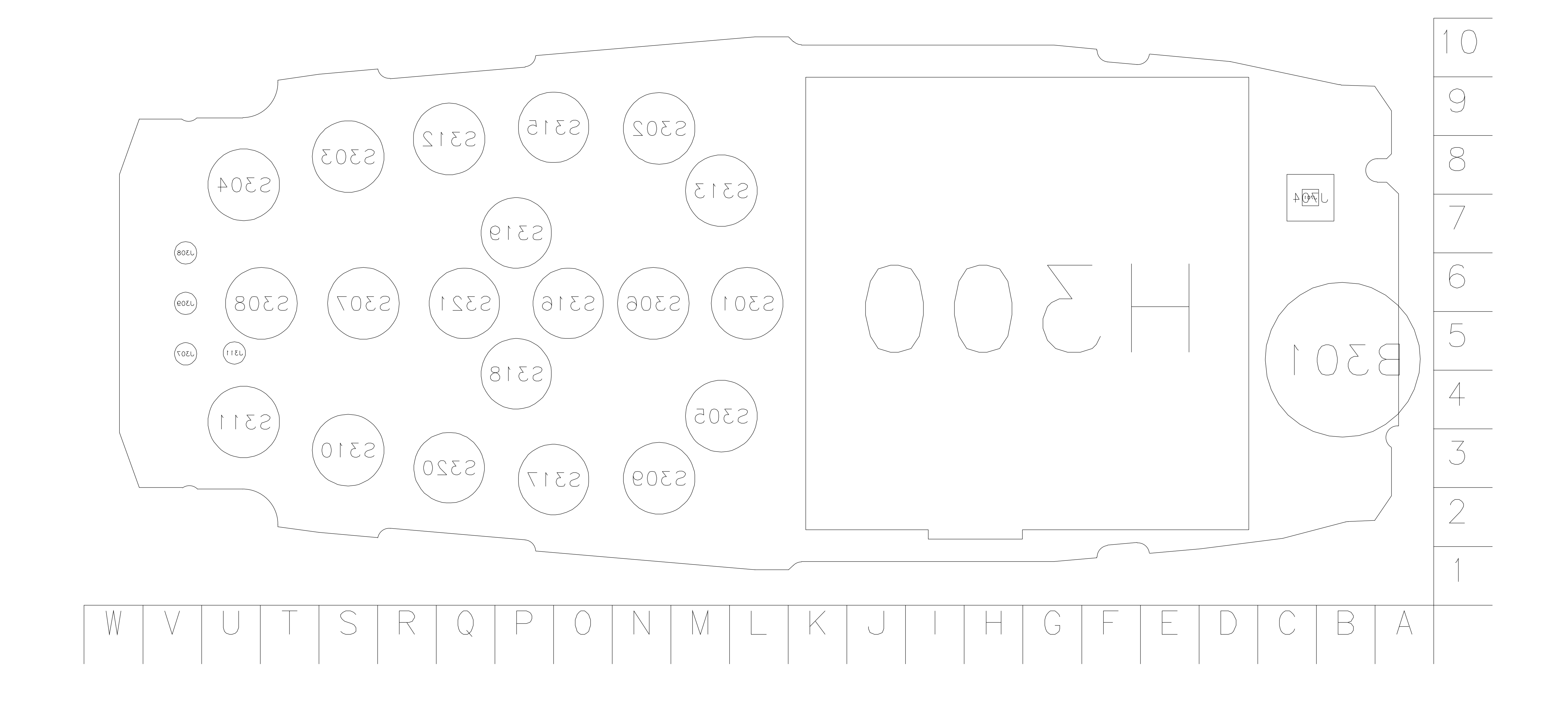
GND AGND2

Power amplifier



Mjoelner




S304
S303
S312
S315
S302
S313
S319
S308
S307
S321
S316
S306
S301
S318
S311
S310
S320
S317
S309
S305
H300
B301
10
9
8
7
6
5
4
3
2
1
W
V
U
T
S
R
Q
P
O
N
M
L
K
J
I
H
G
F
E
D
C
B
A

